

M. S. LIN MEG-99-005C



July 6, 2005

*Do not  
enter  
7-21-05  
DW*

To: Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Fr: Stephen B. Ackerman, Reg. No. 37,761  
28 Davis Avenue  
Poughkeepsie, N.Y. 12603

Subject:

Serial No. 09/523,990 03/13/00

M. S. LIN

"METHOD OF MANUFACTURE AND IDENTIFICATION  
OF SEMICONDUCTOR CHIP MARKED FOR  
IDENTIFICATION WITH INTERNAL MARKING  
INDICIA AND PROTECTION THEREOF BY NON-  
BLACK LAYER AND DEVICE PRODUCED THEREBY"

Group Art Unit: 2876

WALSH, DANIEL I.

RESPONSE TO OFFICE ACTION AND PRELIMINARY AMENDMENT

Dear Sir:

In response to the Office Action dated April 06, 2005, please amend the above-identified application for patent as follows:

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on July 6, 2005.

Stephen B. Ackerman, Reg. No. 37,761

Signature *SBA*  
Date 7/6/05

07/11/2005 CCHAU1 00000069 190033 09523990

01 FC:2202 50.00 DA